

Fig. 1

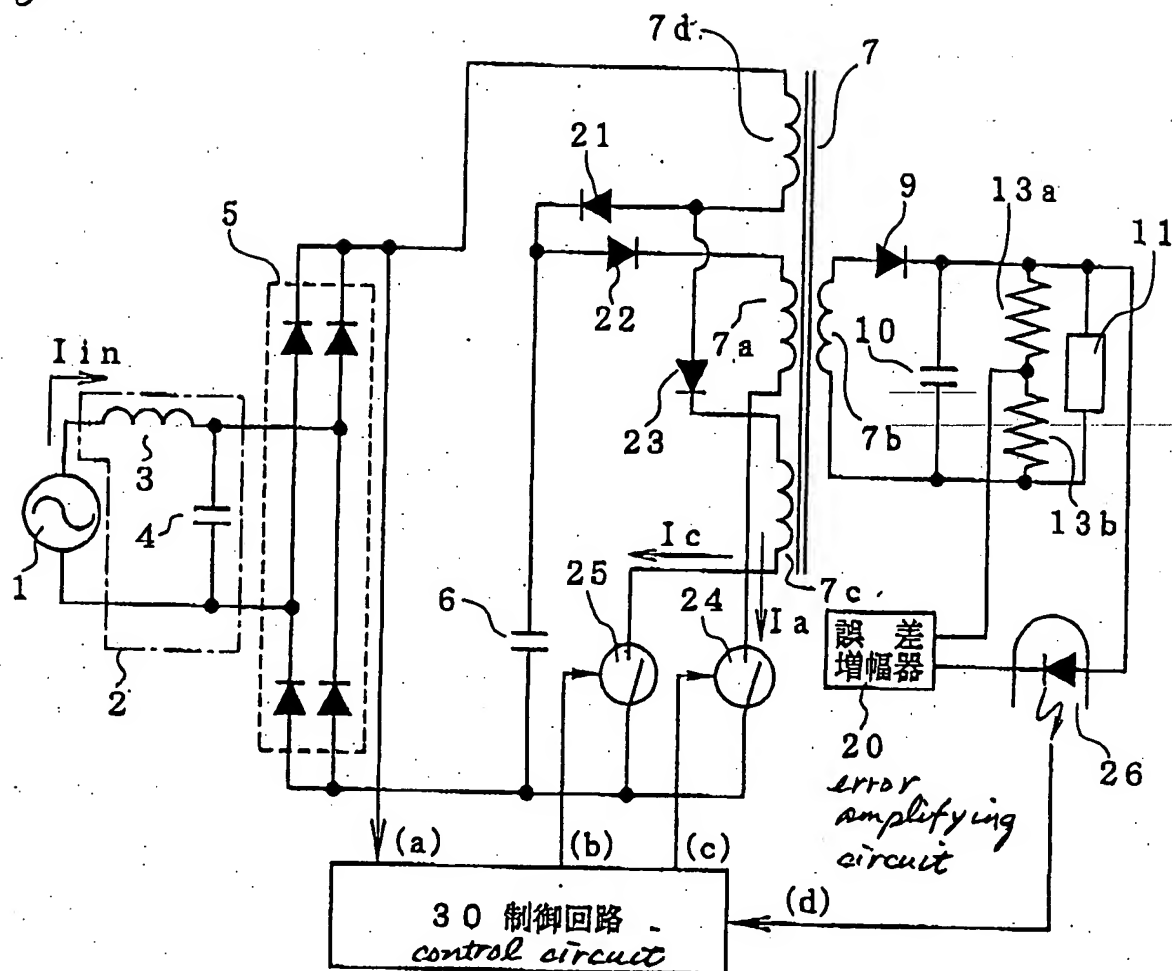
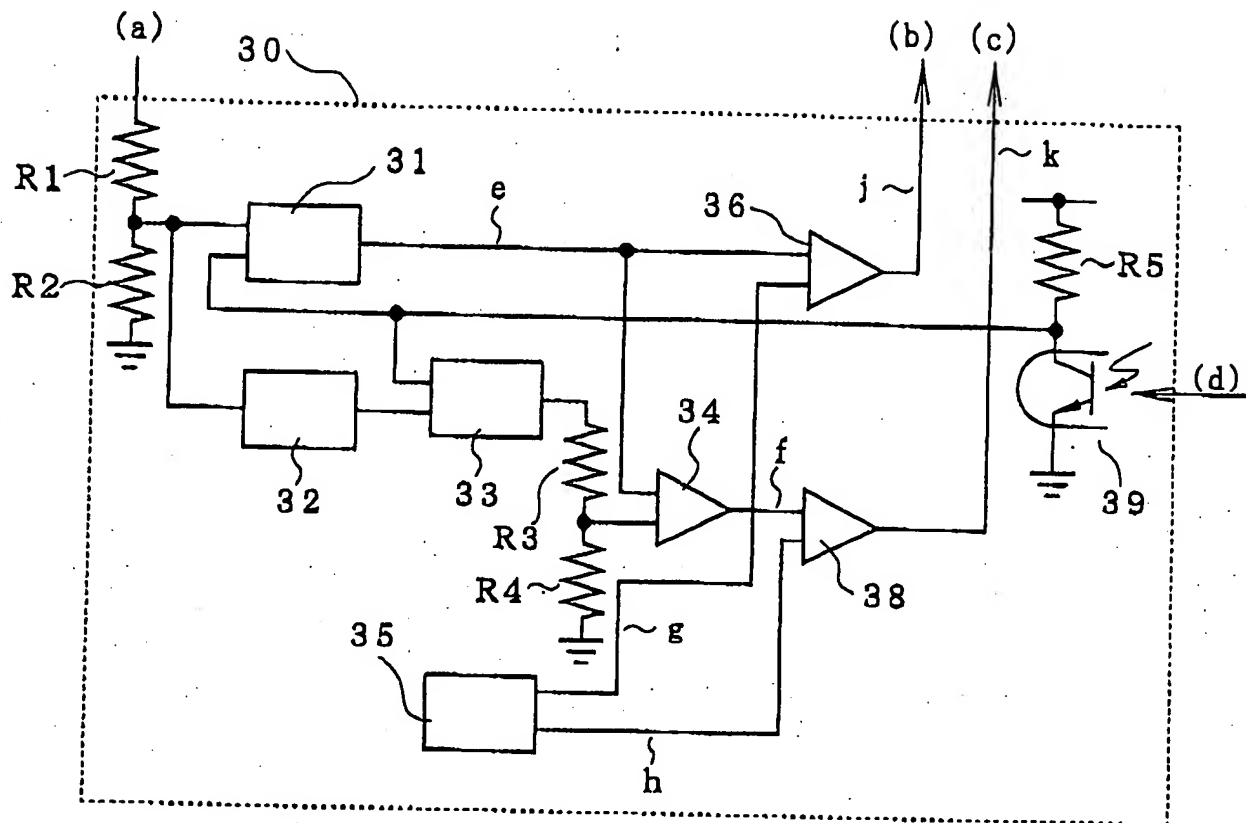


Fig. 2



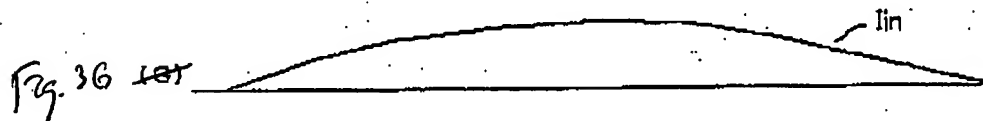
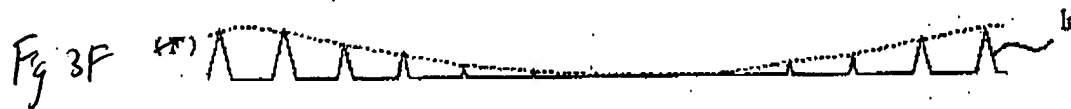
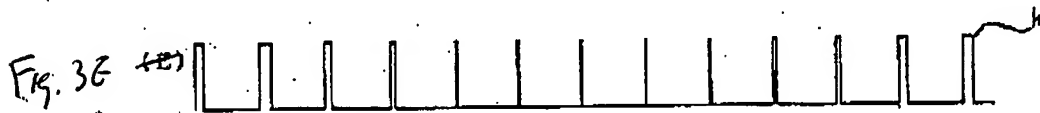
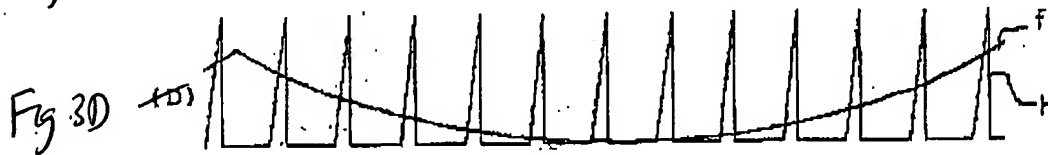
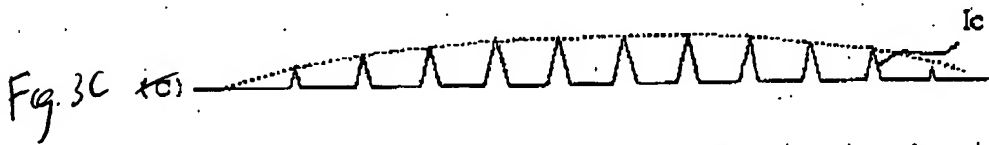
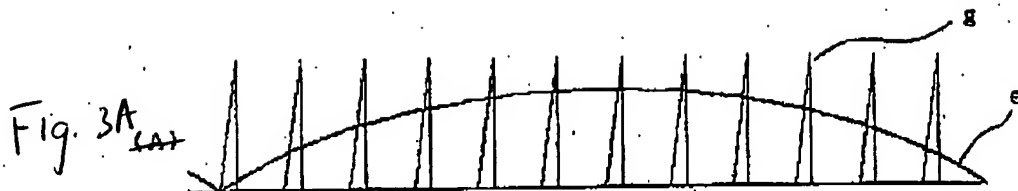


Fig. 4

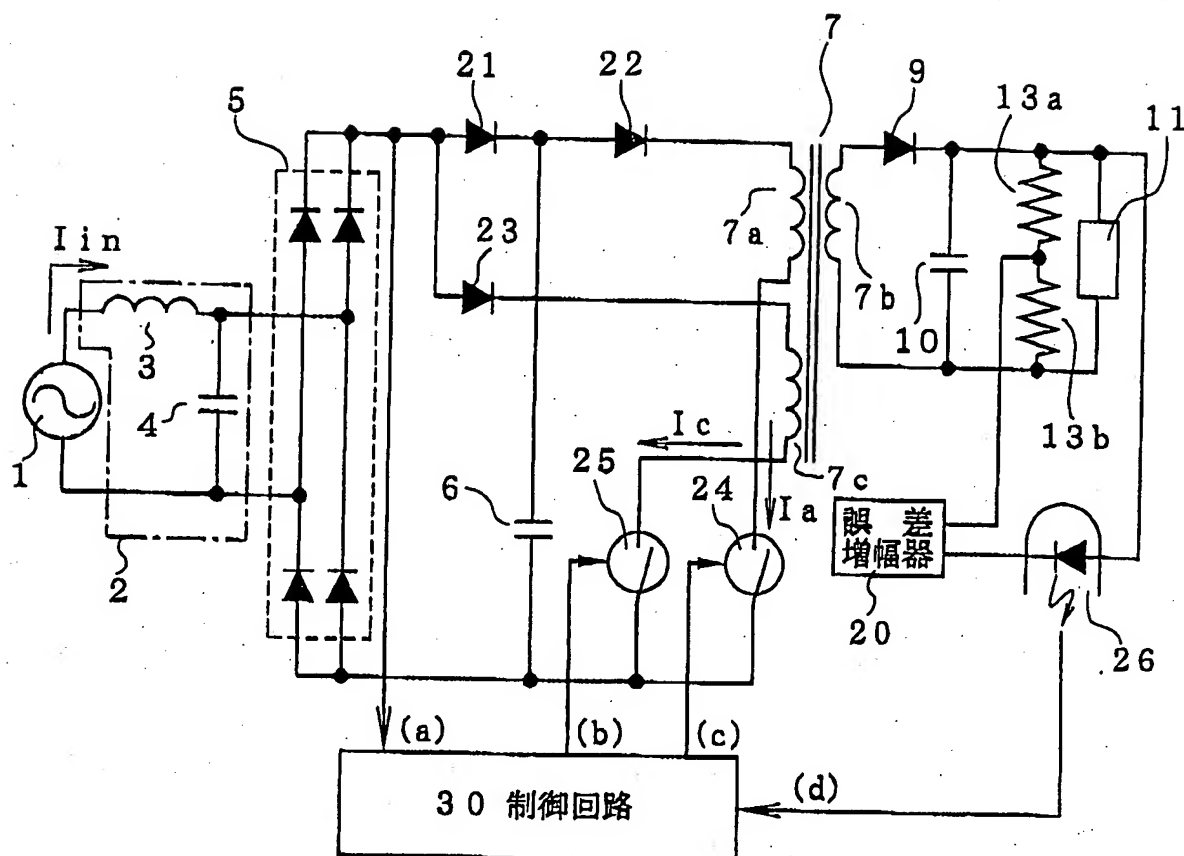


Fig. 5 (Prior Art)

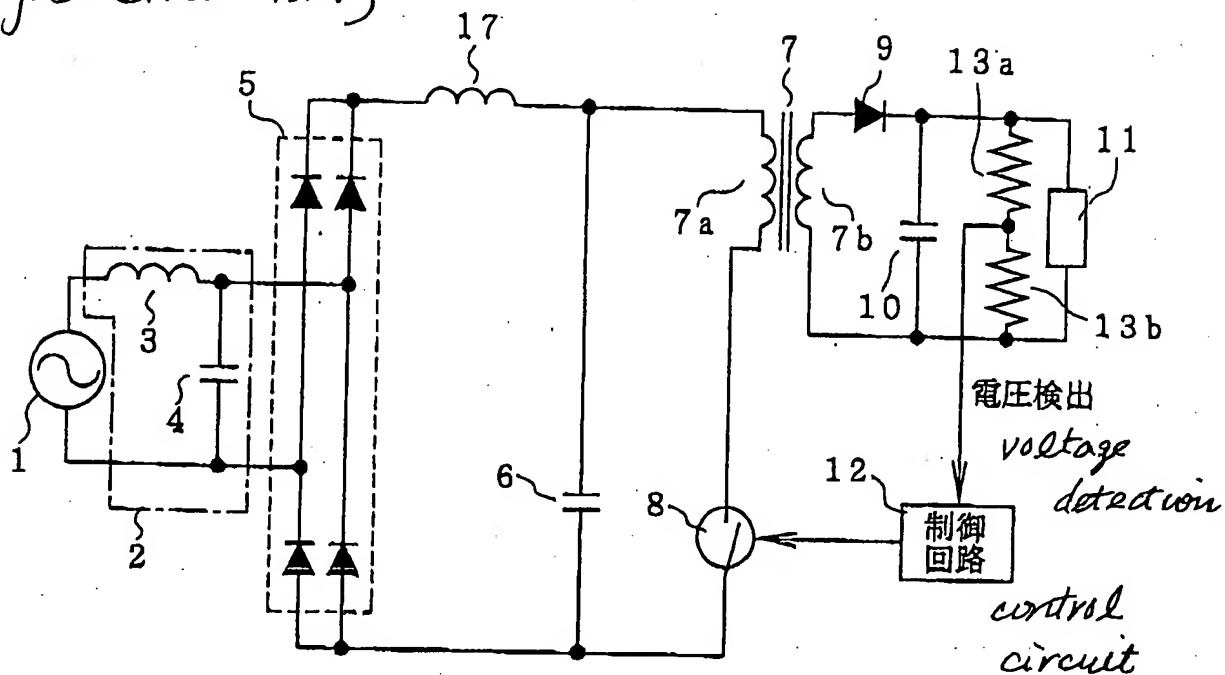


Fig. 6 (Prior Art) *voltage detection*

